




ANDHRA LOYOLA INSTITUTE OF ENGINEERING AND TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTU Kakinada)

Accredited by NAAC & An ISO 9001:2015 Certified Institution

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FACULTY PROFILE

<p>Name of the Faculty</p>	<p>Mr. P.BOSEBABU</p>		
<p>Designation</p>	<p>Assistant Professor</p>		
<p>Department</p>	<p>Electronics & Communication Engineering (ECE)</p>	<p>Date of Birth 26/08/1988</p>	
<p>Date of Joining the Institution</p>	<p>06-06-2012</p>	<p>Native place Kollipara, AP, India</p>	
<p>Academic Qualification with Class/Grade</p>	<ul style="list-style-type: none"> • Pursuing Ph.D from Andhra University • PG: M.Tech in VLSI System Design with with Distinction from JNTUK. • UG: B.Tech in ECE with First Class from JNTUK. 		
<p>Professional Qualification</p>	<ul style="list-style-type: none"> • Certified as a ProAct Digital Teacher from Loyola Institute of Business Management- (LIBA) June 2020. 		
<p>Employee ID</p>	<p>ALIET-12-04</p>		
<p>E-mail / Mobile</p>	<p>pbosebabu@aliet.ac.in +91 9440729045</p>		
<p>Total Experience in Years</p>	<p>Teaching: 09 years</p>	<p>Industry 00</p>	<p>Research : 00</p>
<p>Papers Published in Journals -</p>	<p>UGC – 11 A Peer-Reviewed-2</p>	<p>Articles in Books: 0</p>	
<p>Faculty Development Programmes attended:</p>	<p>16</p>	<p>Guest Lectures Delivered: 00</p>	
<p>Papers presented in Conferences/ attended</p>	<p>National: 05</p>	<p>International: 02 / Workshops: 04</p>	
<p>PhDs / Projects Guided</p>	<p>PhDs : 00 MPhil -00</p>	<p>Projects at PG Level : 04 Projects at UG Level: 18</p>	
<p>Professional Memberships</p>	<p>1.Associate Member of Institute of Engineers 2.Member of Indian Society of Systems for</p>		

	Science & Engineering
Conferences/ Seminars / Workshops / FDP Organised @ ALIET	<ul style="list-style-type: none"> • Organized Advanced Algorithms In Digital Signal Processing for the academic year 2015-16. • Organized a APSSDC FDP in the academic year 2016-17 • Organized EDC Cell programs of department for the academic year 2018-19
Any Other Achievements	<ul style="list-style-type: none"> ✓ Qualified GATE Examination in 2008, 2009 & 2014. ✓ Qualified as Asst.Prof. in UGC-NET 2019
Other Responsibilities	<ul style="list-style-type: none"> • NAAC coordinator for ECE department • ACADEMIC coordinator for ECE department
Whether Ratified by University (Yes/No)	YES
Faculty Development Programmes attended: 2019-20	
<ol style="list-style-type: none"> 1. Participated in a five day national level online FDP on “modern trends in Communication, signal processing and VLSI/MEMS”, organized by ALIET, from 19th to 23rd May 2020. 2. Participated in a two day online FDP on “Micro strip antennas for advanced wireless communications”, organized by KHIT, from 15th to 16th May 2020. 3. Participated in a three day national level online FDP on “COVID-19 as global crisis: application and appreciation of language and literature “, organized by ALIET, from 12th to 14th May 2020. 4. Participated in a five day online FDP on “Biopython”, organized by SIST, from 4th to 8th May 2020. 5. Participated in a four day online FDP on “Digital image processing using MATLAB “, organized by skilltohire, from 24th to 27th April 2020. 	
Online Webinars Attended: 2019-20	
<ol style="list-style-type: none"> 1. Participated in the webinar “A webinar on examination reforms to complement OBE requirements“, conducted by KL University, Vijayawada on 24th April 2020. 2. Participated in the webinar “A webinar on machine learning techniques for detection and classification“, conducted by KL University, Vijayawada on 25th April 2020. 3. Participated in the webinar “Artificial Intelligence & Machine learning-Challenges, opportunities and Emerging applications “, conducted by KL University, Vijayawada on 30th April 2020. 4. Participated in the webinar technology programme on “LABVIEW “, conducted by Labview academy, SJBIT, Bangalore from 04th to 8th May 2020. 5. Participated in the webinar “the need for change in pedagogy and a simple way to teach online with smart phone for beginners “, conducted by UCET, Telaprolu on 11th May 2020. 6. Participated in the webinar “Faculty awareness programme on outcome based education and NBA 	

accreditation “, conducted by SITS, Pune on 14th May 2020.

7. Participated in the webinar “Project management webinar 2.0-feasibility study in project management –A case study “, conducted by LBRCE, Mylavaram on 16th May 2020.
8. Participated in the webinar “Classification of biomedical images using Whale optimization algorithm“, conducted by MEC, Rasipuram, on 20th May 2020
9. Participated in the webinar “Guidelines for writing a Research paper “, conducted by AEC, Thanjavur, 22nd May 2020.

Online Quiz Programme attended: 2019-20

1. Participated in the “Faculty program on NBA” conducted by BVEC, Mumbai, on 9th May 2020.
2. Participated in the “Quiz on recent trends in IT” conducted by SLRTCE, Bangalore, on 9th May 2020.
3. Participated in the “Quiz on Electrical Engineering” conducted by MCET, Hyderabad, on 21st May 2020.
4. Participated in the “National level Quiz on NAAC” conducted by PVPPCE, Mumbai, on 26th May 2020.
5. Participated in the “Technical Quiz contest” conducted by SVCE, chittoor, on 30th May 2020.

List of Publications in Journals

- 1.** Published a paper with “VLSI architecture for zero frequency filter “ as title in IJARI, vol-8, 2020
- 2.** Published a paper with “A novel hardware security mechanism using hybrid cryptography algorithms “ as title in IJRECE, vol-7, June 2019
- 3.** Published a paper with “Detection and Prevention of animals in agricultural fields “ as title in IJRECE, vol-7, June 2019
- 4.** Published a paper with “Automatic segmentation of layers in outer retina by model selection “ as title in IJESAT, vol-8, 2018
- 5.** Published a paper with “A Technique for intensity normalisation of finger vein image “ as title in IJCRT, vol-6, 2018
- 6.** Published a paper with “An adaptive single image dehazing algorithm “ as title in JETIR, vol-5, 2018
- 7.** Published a paper with “An efficient 16 point reconfigurable DCT architecture using improved 8 point DCT “ as title in IJMTST, vol-3, 2017
- 8.** Published a paper with “FPGA implementation of Multi ordered FIR filters with low complexity “ as title in discovery journal, 2015
- 9.** Published a paper with “A primitive polynomial to define the finite field modules for high throughput interpolator to encode and decode of non binary cyclic codes “ as title in IJMER, vol-2, 2012
- 10.** Published a paper with “Design of modules to implement a structure by discrete reckoning codes for embedding into video coding testing applications “ as title in IJMER, vol-2, 2012
- 11.** Published a paper with “A low power adder using reversible logic gates “ as title in IJRET, vol-1, 2012

List of Publications (A Peer-Reviewed Journal)

1. Published a paper with “Perforated serpentine membrane with AIN as dielectric material shunt capacitive RF MEMS switch fabrication and characterization” as title in Springer-2020
2. Published a paper with “Reduction of Hardware Trojan effect using Multi path authentication “ as title in IJST,vol-9,june 2020

National Seminars: 01

1. Attended a national seminar on ”MATLAB,Simulink&related tool boxes for engineering education” in the year 2015.

Workshops:

1. Participated in the two day workshop “Industrial IoT using NI Graphical System Design Platform“, conducted by National Instruments, APITA, Bangalore , from 14th to 15th July 2017.
2. Participated in the two day workshop “Advances in mathematical models for image processing”, PVPSIT, Vijayawada, from 4th to 5th March 2016.
3. Participated in the two day workshop “NI-Labview”, ALIET, Vijayawada, from 19th to 22nd September, 2016.
4. Participated in the two day workshop “System Design Using Xilinx FPGAs”, ALIET, Vijayawada, from 27th to 28th June, 2013.