




ANDHRA LOYOLA INSTITUTE OF ENGINEERING AND TECHNOLOGY

(Approved by AICTE, New Delhi & Affiliated to JNTU Kakinada)

Accredited by NAAC & An ISO 9001:2015 Certified Institution

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FACULTY PROFILE

<p>Name of the Faculty</p>	<p>Ms.Saggurthi Spandana</p>		
<p>Designation</p>	<p>Assistant Professor</p>		
<p>Department</p>	<p>ELECTRONICS AND COMMUNICATION ENGINEERING</p>		
<p>Date of Joining the Institution</p>	<p>01-02-2021</p>	<p>Native place Vijayawada, AP, India</p>	
<p>Academic Qualification with Class/Grade</p>	<ul style="list-style-type: none"> • PG : M.Tech (ECE) First Class with Distinction from JNTUK. • UG : B.Tech (ECE) First Class with Distinction from JNTUK. 		
<p>Employee ID</p>	<p>ALIET-21-09</p>		
<p>E-mail / Mobile</p>	<p>sspandana2017@gmail.com</p>		
<p>Total Experience in Years</p>	<p>Teaching: 3 years 7 months</p>	<p>Industry 00</p>	<p>Research : 3 years 7 months</p>
<p>Papers Published in Journals -</p>	<p>UGC – 7</p>		
<p>Faculty Development Programmes attended:</p>	<p>6</p>		
<p>Papers presented in Conferences/ attended</p>	<p>National: 3</p>	<p>International: 04 / Workshops:</p>	
<p>PhDs / Projects Guided</p>	<p>PhDs : 00 MPhil -00</p>	<p>Projects at PG Level : 00 Projects at UG Level: - 4</p>	
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Workshops and Conferences:

- Attended International Conference On Technological Emerging Challenges **ICTEC-2019** in **Tirumala Engineering College**.
- Attended Faculty Development Programme on FinFet Technology during 13/11/2018-19/11/2018.
- Attended Faculty Development Programme on VLSI chip design hands-on using open source EDA
- Attended Faculty Development Programme on Networking and cyber security
- Attended Faculty Development Programme on Technological Advances in Power Switching Converters for Renewable Energy Sources and Fuel Cell.
- Attended Faculty Development Programme on Internet of Things and Artificial Intelligence Applications.

Faculty Development Programmes attended: 2019-20

1. Attended FDP on “**Antenna Trends**” jointly organized by electronics & ICT academics.
2. Attended FDP on “**VLSI Chip Design Hands-on using open source EDA**” jointly organized by electronics & ICT academics.
3. Attended Faculty Development Programme on **FinFet Technology**

Online Webinars Attended: 2019-20

1. Attended webinar on “**Role of electronics in Analytical Instrumentation**”.
2. Attended webinar on “**Introduction to Informatica Power Center**”.
3. Attended webinar on “**Tools for making your Thesis complete**”.
4. Attended webinar on “**Ensuring Quality Assurance In Higher Education – Role Of Industry & Professional Bodies**”.
5. Attended webinar on “**Future Trends In Cmos Technology At 5nm And Beyond**”.
6. Attended webinar on “**Modern Antennas for Wireless Systems**”.

Online Quiz Programme attended: 2019-20

1. Attended Online quiz on “**Fundamental of Computer**”.
2. Attended Online quiz on “**Bio Medical Engineering**”.
3. Attended Online quiz on “**Analog Communication**”.
4. Attended Online quiz on “**Web Designing**”.
5. Attended Online quiz on “**Internet Of Things**”.
6. Attended Online quiz on “**AERO Quiz – 20**”.

List of Publications in Journals

- Paper Published in “International Journal of Innovative Research in Computer and Communication Engineering” with the title “**Performance Analysis of Saliency Detection in Stereoscopic Images**”.
- Paper Published in “International Journal of Journal of Emerging Technologies and Innovative Research with the title “**Designing Efficient Circuit Based on RunTime Reconfigurable Field Effect Transistors Using Enhanced CMOS Design**”.
- Paper Published in “International Journal of Research in Advent Technology”, ISSN: 2321-9637 with the title “**Image Saliency Detection in Uncompressed Domain**”.
- Paper Published in “International Journal of Research in Advent Technology”, ISSN: 2321-9637 with the title “**Implementation of Health Monitoring System using 8051**”.
- Paper Published in “International Journal of Research in Advent Technology”, ISSN: 2321-9637 with the title “**Vehicle Blog Box**”.
- Paper Published in “International Journal of Research in Advent Technology”, ISSN: 2321-9637 with the title “**Advanced Image Forgery Localization Using Labeled Feature Points Extraction**”.