

ANDHRA LOYOLA INSTITUTE OF ENGINEERING AND TECHNOLOGY



(Approved by AICTE, Affiliated to JNTUK & Accredited by NAAC)
Vijayawada-520008

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

(Accredited by NBA)

INSTITUTION OF ENGINEERS (INDIA) STUDENTS' CHAPTER

(Code: 520008/ALIT/EE)

The Institution of Engineers (India) IE(I) head quartered at Kolkata, aims to promote the needs of the student community, acts as a platform to connect with global engineers and actively encourages R&D programs through various promotional aids. In addition, supporting many types of activities such as Lectures, Seminars, Symposia, Workshops and other educational programs.

The Institution of Engineers India (IEI) EEE Students Chapter at Andhra Loyola Institute of Engineering and Technology: Vijayawada is recognized as an authorized Students' Chapter. We have started IE (I) Student's Chapter on 14th Oct 2022. Currently, our IEI Students' Chapter (EEE) consists of many students and faculty members.



Objectives

- To facilitate the exchange of information and ideas, amongst the members and the persons attached to the Institution
- To inculcating and promoting the technical instinct among the students and as a platform for the technical proceedings
- To get the students more acquainted with the existing technology and to make familiar with the state-of-art technology.
- To promote the general advancement of engineering their applications.

Outcomes:

IEI Student Chapter provides students with the opportunity to

- Implement the technical knowledge which they get from guest lecture, workshops and project exhibitions.
- Enhance their thinking ability and build a leadership quality to pursuit their career growth.

This professional society also facilitates IEI student members to participate in technical events e.g. Seminars, Symposia, Conventions, Workshops etc. These are organized by various IEI centers at State, National and International levels with a concession. Scholarship aids and Research grants will also be provided by IEI. Under the banner of IEI, various technical events like seminars, industrial visits, group discussions, technical quiz competitions and workshops are organized in our campus.

FACULTY ADVISER(S):

S.NO.	NAME	E mail:	Contact No.
1	Dr. M. Ajay Kumar, Assoc. Prof.	ajaykumar@aliet.ac.in	9491827298
2	Mr. M. Ramakrishna, Asst. Prof.	mramakrishna2009@gmail.com	9293731842

OFFICE BEARERS:(A.Y 2022-2023)

President	M. Laya (20HP1A0202)	
Vice President	V. Sivarama Sharma(21HP1A0218)	
Secretary	K. Naveen (20HP1A0232)	
Technical Event Organizers	P. Kumidini (20HP1A0205)	
	N. Chaitanya (21HP1A0202)	
	I. Vasu (21HP1A0228)	
	R.Sankar(22HP5A0212)	

Activities (A.Y. 2022-23):

S.NO.	DATE	Resource Person	Title of the Event
1	17-02-2023	Er.T.Sreemannar-	Overview of Electrical Power
		ayana murthy,	Generation, Transmission and
		DE, AP, Vidhyuth	Distribution: Practical aspects
		soudha,	
		Vijayawada.	
2	23-02-2023	Mr. L Karunakar	Technical Quiz competition
3	27-28 th Feb	Mr. Venkata	Three day Workshop on "Electric
	23	Manikiran, SKILL	Vehicle Design"
		Dzire, AP.	
4	27-03-2023	Dr.M. Ajay	PROJECT EXPO-2K23
		kumar	

Activities (A.Y. 2023-24)

Title of the Event: Hands on Workshop on "Advanced Digital System Design Using Verilog HDL"

Date & Duration: 21st July, 2023; 9 a.m -5p.m

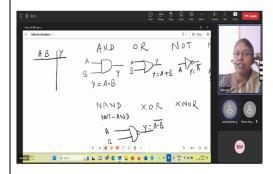
Venue: Electrical simulation lab

Objectives:

- ✓ To enhance the knowledge of student and motivate them to compete in the global competitive engineering field
- ✓ To impart awareness about Verilog Modeling Styles, Combinational and Sequential Circuits
- ✓ To develop a new and innovative projects which will improving quality, environment and economic development of nation

Resource Person(s): Dr. O.V. Gnana Swathika, CSGT, VIT Dr. K. Jamuna, SELECT, VIT

Summary: The resource person **Dr. O V Gnana Swathika**, explaining about the different types of programming languages, difference between Verilog and Verilog HDL to check the truth tables, and analysis the output for dataflow, behaviour and structural Circuits. The students learnt about the Verilog HDL software, how to write the programs in easyplayground software to check the truth tables.





Outcome: The student will be able to analyze various Verilog descriptions for Combinational and sequential circuits.

Title of the Event: Speech activity on Role of Renewables in sustainability

Date & Duration: 26th July, 2023; 3:20p.m -4:30p.m

Venue: Lecture Hall- 4E4

Objectives:

✓ To develop the Presentation Skills, discussion Skills, and listening Skills.

✓ To enhance the knowledge of student and motivate them to compete in the global competitive engineering field.

Resource Person(s): Dr. G. Naveen kumar Mr. L.Karunakar

Summary: A Topic for presentation namely "Role of Renewables in Sustainability" was given to students to present their view points. Based on their speech, their presentation, Body Language, presentation skills, public speaking ability and stage handling skills were judged and given a feedback to improve on lacking areas.





Outcome: The students will develop and evaluate ideas and arguments.

Title of the Event: Inter department Quiz competition-2K23

Date & Duration: 23/11/2023; 2 P.M - 5 P.M

Venue: South Seminar Hall

Objectives:

- ✓ To evaluate students' understanding of technical concepts, theories, and principles relevant to a specific field or subject.
- ✓ To Identify areas of improvement for students and encourage them to enhance their technical skills through targeted learning and practice.

Resource Person: Dr. V. Anantha Lakshmi

Summary: The inter department quiz competition was conducted on 23/11/2023. To filter out the participants preliminary test which contained 15 questions was conducted at 1.15pm

on the same day. Totally 94 participants (from 2nd year,3rd year & 4th year) wrote the preliminary test. For the final quiz competition 21 (7 members from each year) were selected out of 94 participants. Formed seven groups with one participant from each year.





Outcome: The student will acquire new information, explore different perspectives, and gain insights into emerging trends in their field.